1/15/05

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

| Title of Invention | METHOD AND MECHANISM FOR DIAGNOSING COMPUTER APPLICATIONS USING TRACES |

Application Number :   09/872647

Confirmation Number: 1722

First Named Applicant: Alok SRIVASTAVA

Attorney Docket Number: 254/254

Art Unit: 2124

Examiner: Satish Rampuria

Search string: (6202099 or 6339775 or 6732095 or 20020016771 or 20020087592 or 20020087949 or 20020120634 or 20020161672).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6202099	2001-03-13	Gillies et al.			
	2	6339775	2002-01-15	Zamanian et al.			
	3	6732095	2004-05-04	Warshavsky et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020016771	2002-02-07	Carothers et al.			
	2	20020087592	2002-07-04	Ghani			
	3	20020087949	2002-07-04	Golender et al.			
	4	20020120634	2002-08-29	Min et al.			
	5	20020161672	2002-10-31	Banks et al.			

Signature

Examiner Name	Date
Satish S. Rampuria	1/5/06

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

				Complete if Known	
				Application Number	09/872,647
				Filing Date	May 31, 2001
				First Named Inventor	Alok SRIVASTAVA
				Art Unit	2124
				Examiner Name	Satish Rampuria
Sheet	1		1	Attorney Docket Number	254/254; OI7011402001

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T[2]
	1	BISWAS, P. et al. "Trace Driven Analysis of Write Caching Policies for Disks" *Proceedings of the 1993 ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems* (1993) pp. 13-23	
	2	BORG, A. et al. "Generation and Analysis of Very Long Address Traces" *Proceedings of the 17th Annual International Symposium on Computer Architecture* (May 1990) 18(3):270-279	
	3	CHANG, P. P. and W.-M. W. HWU "Trace Selection for Compiling Large C Application Programs to Microcode" *Proceedings of the 21st Annual Workshop on Microprogramming and Microarchitecture* (Jan. 1988) pp. 21-29	
	4	COLBY, C. and P. LEE "Trace-Based Program Analysis" *Proceedings of the 23rd ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages"* (Jan. 1996) pp. 195-207	
	5	DAN, A. et al. "Characterization of Database Access Pattern for Analytic Prediction of Buffer Hit Probability" *VLBD Journal* (1995) 4:127-154	
	6	DUCASSE, M. "Coca: An automated Debugger for C" *Proceedings of the 21st International Conference on Software Engineering* (May 1999) pp. 504-513	
	7	EZUST, S.A. and G. v. BOCHMANN "An Automatic Trace Analysis Tool Generator for Estelle Specifications" *Proceedings of the Conference on Applications, Technologies, Architectures and Protocols for Computer Communication* (Oct. 1995) 25(4):175-184	
	8	GOLDSZMIDT, G. S. and S. YEMINI "High-Level Language Debugging for Concurrent Programs" *ACM Transactions on Computer Systems (TOCS)* (Nov. 1990) 8(4):311-336	
	9	HSU, W. W. et al. "I/O Reference Behavior of Production Database Workloads and the TPC Benchmarks - An Analysis at the Logical Level" *ACM Transactions on Database Systems (TODS)* (Mar. 2001) 26(1):96-143	
	10	LeDOUX, C. H. and D. S. PARKER, Jr. "Saving Traces for ADA Debugging" *Proceedings of the 1985 Annual ACM SIGAda International Conference on Ada* (May 1985) V(2):97-108	
	11	LENCEVICIUS, R. et al. "Third Eye - Specification-Based Analysis of Software Execution Traces" *Proceedings of the 22nd International Conference on Software Engineering* (June 2000) p. 772	
	12	LO, J. L. et al. "An Analysis of Database Workload Performance on Simultaneous Multithreaded Processors" *Proceedings of the 25th Annual International Symposium on Computer Architecture* (Apr. 1998) 26(3):39-50	
	13	MILLER, B.P. and J.-D. CHOI "A Mechanism for Efficient Debugging of Parallel Programs" *Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation* (June 1988) pp. 135-144	
	14	PUTRYCZ, E. "Using Trace Analysis for Improving Performance in COTS Systems" *Proceedings of the 2004 Conference of the Centre for Advanced Studies on Collaborative Research* (Oct. 2004) pp. 1-13	
	15	RAHM. E. "Empirical Performance Evaluation of Concurrency and Coherency Control Protocols for Database Sharing Systems" *ACM Transactions on Database Systems (TODS)* (June 1993) 18(2):333-377	
	16	RAMAKRISHNAN, K. K. "Analysis of File I/O Traces in Commercial Computing Environments" *Proceedings of the 1992 ACM SIGMETRICS Joint International Conference on Measurement and Modeling of Computer Systems* (June 1992) 20(1):78-90	
	17	REISS, S.P. and M. RENIERIS "Generating Java Trace Data" *Proceedings of the ACM 2000 Conference on Java Grande* (June 2000) pp. 71-77	
	18	SINGHAL, V. and A.J. SMITH "Analysis of locking behavior in three real database systems" *The VLDB Journal* (1997) 6:40-52	

Examiner Signature		Date Considered	1/5/06	

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Title of Invention	METHOD AND MECHANISM FOR DIAGNOSING COMPUTER APPLICATIONS USING TRACES

Application Number : 09/872647

Confirmation Number: 1722

First Named Applicant: Alok SRIVASTAVA

Attorney Docket Number: 254/254

Art Unit: 2191

Examiner: Satish Rampuria

Search string: (3707725 or 5537319 or 5594904 or 5870606 or 5963740 or 6083281 or 6164841 or 6189141 or 6202199 or 6289503 or 6311326 or 6338159 or 6351844 or 6654749 or 6658652 or 6678883 or 6748583 or 6754890 or 6802054 or 6826747 or 20010011360 or 20030088854 or 20040158819 or 20040205718 or 20040216091 or 20040216092).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	3707725	1972-12-16	Dellhelm			
	2	5537319	1996-07-16	Schoen			
	3	5594904	1997-01-14	Linnermark et al.			
	4	5870606	1999-02-09	Lindsey			
	5	5963740	1999-10-05	Srivastava et al.			
	6	6083281	2000-07-04	Diec et al.			
	7	6164841	2000-12-26	Mattson, Jr. et al.			
	8	6189141	2001-02-13	Benitez et al.			
	9	6202199	2001-03-13	Wygodny et al.			
	10	6289503	2001-09-11	Berry et al.			
	11	6311326	2001-10-30	Shagam			
	12	6338159	2002-01-08	Alexander, III et al.			
	13	6351844	2002-02-26	Bala			
	14	6654749	2003-11-25	Nashed			
	15	6658652	2003-12-02	Alexander, III et al.			
	16	6678883	2004-01-13	Berry et al.			
	17	6748583	2004-06-08	Aizenbud-Reshef et al.			
	18	6754890	2004-06-22	Berry et al.			

init		19	6802054	2004-10-05	Faraj			
		20	6826747	2004-11-30	Augsburg et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20010011360	2001-08-02	Shigeta			
	2	20030088854	2003-05-08	Wygodny et al.			
	3	20040158819	2004-08-12	Cuomo et al.			
	4	20040205718	2004-10-14	Reynders			
	5	20040216091	2004-10-28	Groeschel			
	6	20040216092	2004-10-28	Ayers et al.			

Remarks

Note: Remarks are not for responding to an office action.

A separate Information Disclosure Statement (IDS) citing Non-Patent Literature documents is being filed under separate cover today. The fee for this IDS is attached to this electronic submission.

Signature

Examiner Name	Date
Satish S. Rampuria	1/5/06
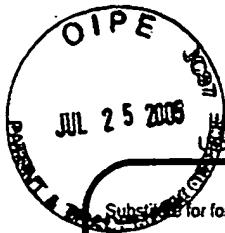
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*	Complete if Known	
	Application Number	09/872,647
	Filing Date	May 31, 2001
	First Named Inventor	Alok SRIVASTAVA
	Art Unit	2191
	Examiner Name	Satish Rampuria

Substitute for form 1449B-PTO

Sheet	1	of	1	Attorney Docket Number	254/254; OI7011402001

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Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T[2]
SR	1	IBM Technical Disclosure Bulletin "Checkref: Method for Effectively Identifying IBM Bookmaster Document Markup Language Coding Errors" (Feb. 1994) pp. 379-392	

Examiner's Signature		Date Considered 1/5/05	

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